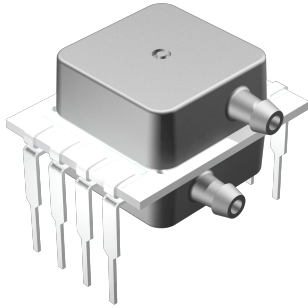# DLV Series Low Voltage Digital Pressure Sensors

Features

• 5 to 60 psi Pressure Ranges
• 3.3V or 5.0V Supply Voltage
• I2C or SPI Interface
• Better than 0.5% Accuracy Over Temperature Typical

Applications

• Medical Breathing
• Environmental Controls
• HVAC
• Industrial Controls
• Portable/Hand-Held Equipment

General Description

The DLV Series Mini Digital Output Sensor is based on the already popular DLVR series pressure sensors. This series offers excellent performance over middle pressure ranges of 5 psi to 60 psi compared to the DLVR low pressure range products.

The supply voltage options ease integration of the sensors into a wide range of process control and measurement systems, allowing direct connection to either I2C or SPI serial communications channels. For battery-powered systems, the sensors can enter very low-power modes between readings to minimize load on the power supply.

These calibrated and compensated sensors provide accurate, stable output over a wide temperature range. This series is intended for use with non-corrosive, non-ionic working fluids such as air, dry gases and the like. A SII-Gel die coating is added for enhanced media protection.

Standard Pressure Ranges

Device	Operating Range	Proof Pressure	Burst Pressure	Nominal Span
DLV-005G	0 to 5 PSI	20 PSI	40 PSI	±13,107 counts
DLV-015G	0 to 15 PSI	60 PSI	120 PSI	±13,107 counts
DLV-015A	0 to 15 PSIA	60 PSI	120 PSI	±13,107 counts
DLV-030G	0 to 30 PSI	120 PSI	200 PSI	±13,107 counts
DLV-060G	0 to 60 PSI	200 PSI	200 PSI	±13,107 counts
DLV-060A	0 to 60 PSIA	200 PSI	200 PSI	±13,107 counts

Equivalent Circuit

I2C
- Vs
- SCL
- SDA
- INT
- Gnd

SPI
- Vs
- SCLK
- MISO
- SS
- Gnd

Pressure Sensor Maximum Ratings

Supply Voltage (Vs)	6 Vdc
Common Mode Pressure	10 psig
Lead Temperature (soldering 2-4 sec.)	270 °C

Environmental Specifications

Temperature Ranges		
Compensated:	Commercial	0°C to 70°C
	Industrial	-20°C to 85°C
Operating		-25°C to 85 °C
Storage		-40°C to 125 °C
Humidity Limits (non condensing)		0 to 95% RH

Performance Characteristics for DLV Series - Commercial and Industrial Temperature Range

ALL PARAMETERS ARE MEASURED AT 3.3V ±5% OR 5.0V ±5% (DEPENDING ON SELECTED VOLTAGE OPTION) EXCITATION AND ROOM TEMPERATURE UNLESS OTHERWISE SPECIFIED. PRESSURE MEASUREMENTS ARE WITH POSITIVE PRESSURE APPLIED TO PORT B.

Parameter	Min	Typ	Max	Units	Notes
Output Span	-	13,107	-	Dec Count	1
Offset Output					
xxxG (at 0 PSIG)	-	1,638	-	Dec Count	
xxxA (at 0 PSIA)	-	1,638	-	Dec Count	
Total Error Band	-	±0.5	±1.0	%FSS	2
Span Temperature Shift	-	±0.1	-	%FSS	3
Offset Temperature Shift	-	±0.1	-	%FSS	3
Offset Warm-up Shift	-	±0.1	-	%FSS	4
Offset Long Term Drift (One Year)	-	±0.1	-	%FSS	-
Linearity, Hysteresis Error		±0.1	±0.25	%FSS	6
Response Delay					5
Sleep - Wake Pressure	-	0.40	0.50	ms	
Sleep - Wake All	-	1.10	1.40	ms	
Update Rate					5
Fast	-	0.40	1.0	ms	
Noise Reduced	-	1.30	3.1	ms	
Low Power	-	6.5	9.5	ms	
Start-up Time	-	-	10.0	ms	5, 7
Digital Resolution					5
Output Resolution	-	14	-	bit	
No Missing Codes	12	13	-	bit	
Temperature Output					8
Resolution	-	11	-	bit	
Overall Accuracy	-	2	-	°C	
Current Requirement (3.3V Option)					5
Fast	-	2.5	3.2	mA	
Noise Reduced	-	2.5	3.1	mA	
Low Power	-	0.6	0.7	mA	
Sleep (Idle)	-	0.5	5.0	uA	
Current Requirement (5.0V Option)					5
Fast	-	3.5	4.5	mA	
Noise Reduced	-	3.6	4.4	mA	
Low Power	-	0.8	1.0	mA	
Sleep (Idle)	-	0.5	5.0	uA	

See the following page for performance characteristics table notes.

I2C / SPI Electrical Parameters for DLV Series

Parameter	Symbol	Min	Typ	Max	Units	Notes
Input High Level	-	80.0	-	100	% of Vs	5
Input Low Level	-	0	-	20.0	% of Vs	5
Output Low Level	-	-	-	10.0	% of Vs	5
I2C Pull-up Resistor	-	1000	-	-	Ω	5
I2C Load Capacitance on SDA, @ 400 kHz	C_{SDA}	-	-	200	pF	5
I2C Input Capacitance (each pin)	C_{I2C_IN}	-	-	10.0	pF	5

Specification Notes

NOTE 1: THE SPAN IS THE ALGEBRAIC DIFFERENCE BETWEEN FULL SCALE DECIMAL COUNTS AND THE OFFSET DECIMAL COUNTS.

Pressure Output Transfer Function

$$Pressure(Prs.Unit) = 1.25 \times \left(\frac{Pout_{dig} - OS_{dig}}{2^{14}}\right) \times FSS(psi)$$

Where,

$Pout_{dig}$ Is the sensor 14 bit digital output.

OS_{dig} Is the specified digital offset (gage and absolute = 1,638)

$FSS(psi)$ Is the sensor Full Scale Span in psi.

NOTE 2: TOTAL ERROR BAND COMPRISES OF OFFSET AND SPAN TEMPERATURE AND CALIBRATION ERRORS, LINEARITY AND PRESSURE HYSTERISIS ERRORS, OFFSET WARM-UP SHIFT AND LONG TERM OFFSET DRIFT ERRORS.

NOTE 3: SHIFT IS RELATIVE TO 25C.

NOTE 4: SHIFT IS WITHIN THE FIRST HOUR OF EXCITATION APPLIED TO THE DEVICE.

NOTE 5: PARAMETER IS CHARACTERIZED AND NOT 100% TESTED.

NOTE 6: MEASURED AT ONE-HALF FULL SCALE RATED PRESSURE USING BEST STARIGHT LINE CURVE FIT.

NOTE 7: POWER-ON TIME IS TIME FROM POWER BEING APPLIED TO FIRST AVAILABLE PART COMMUNICATIONS.

NOTE 8: TEMPERATURE OUTPUT CONVERSION FUNCTION:

Temperature Output Transfer Function:

$$Temperature\ (°C) = Tout_{dig} \times \left(\frac{200}{2^{11} - 1}\right) - 50$$

All Sensors   DS-0336 Rev A

Device Options

The following is a list of factory programmable options. Consult the factory to learn more about the options.

Interface
I2C and SPI interfaces are available. NOTE: SPI interface is only available with eight (8) lead packages.

Supply Voltage
Devices are characterized at either 3.3V or 5.0V depending on the options selected. It is suggested to select the option that most closely matches the application supply voltage for best possible performance.

Speed/Power
There are four options of Speed/Power. These are Fast(F), Noise Reduced(N), Low Power(L) and Sleep mode(S).

Fast Mode(F) Is the fastest operating mode where the device operates with continuous sampling at the fastest internal speed.

Noise Reduced(N): Also operates with continuous samples however the ADC is set for over sampling for noise reduction. The conversion times are resultantly longer than the Fast(F) mode however, there is approximately 1/2 bit reduction in noise.

Low Power(L): Is similar to the Fast(F) mode with exception that the device uses an internal timer to delay between pressure conversions. The internal timer time-out triggers the next conversion cycle. The update rate is commensurately lower for this mode as a result.

Sleep(S): Is similar to the Low Power(L) mode however the trigger to initiate a sample comes from the user instead of an internal timer. This is ideal for very low update rate applications that require low power usage. It is also ideal for synchronizing the data conversions with the host microprocessor.

Coating

Parylene Coating: Parylene coating provides a moisture barrier and protection form some harsh media. The DLV Series includes a Sil-Gel die coating for enhanced media protection however for additional protection, a Parylene coating may at times be beneficial. Consult factory for applicability of Parylene for the target application and sensor type.

Operation Overview

The DLV is a digital sensor with a signal path that includes a sensing element, a 14 bit analog to digital converter, a DSP and an IO block that supports either an I2C or SPI interface (see Figure 1 below). The sensor also includes an internal temperature reference and associated control logic to support the configured operating mode. The sensing element is powered down while not being sampled to conserve power. Since there is a single ADC, there is also a multiplexer at the front end of the ADC that selects the signal source for the ADC.

Figure 1 - DLV Essential Model
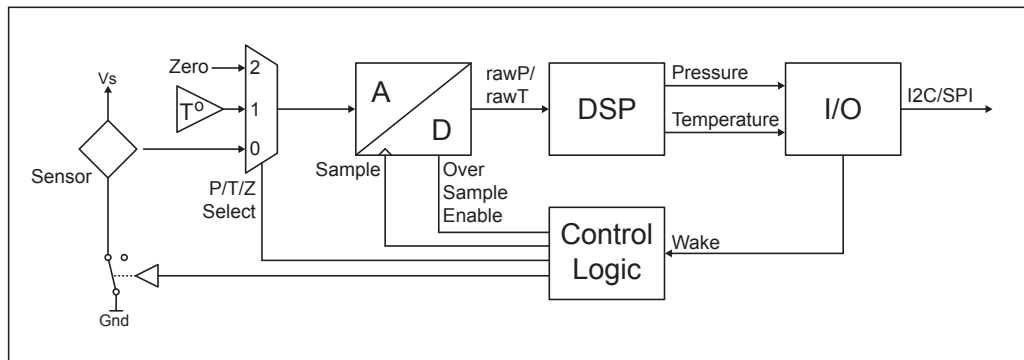
The ADC performs conversions on the raw sensor signal (P), the temperature reference (T) and a zero reference (Z) during an ADC zero cycle. It also has an oversampling mode for a noise reduced output. A conversion cycle that is mesuring pressure is called a Normal cycle. A cycle where either a temperature measurement or zeroing is being performed is called a Special cycle.

The DSP receives the converted pressure and temperature information and applies a multi-order transfer function to compensate the pressure output. This transfer function includes compensation for span, offset, temperature effects of span, temperature effects of offset and second order temperautre effects of both span and offset. There is also linearity compensation for all devices.

There are two effective operating modes of the sensor 1) Free Running and 2) Triggered. The control logic performs the synchronization of the internal functions according the factory programmed Power/Speed option (see Table 1). The Control Logic also determines the Delay between ADC samples, the regularity of the Special cycles and whether or not the ADC performs the Over Sampling. Refer to Figure 2 for the communication model associated with the operating modes listed below.

Free Running Mode: In the free running mode, conversion cycles are initiated internally at regular intervals. There are three options available that operate in the Free Running mode (F, N and L). Two of these (F and N) run continuously while the third option (L) has an approximate 6 ms delay between conversion cycles. All three options have Special cycles inserted at regular intervals to accomplish the ADC zeroing and temperature measurements. Two of the options utilize oversampling. Refer to Table 1 for specific option controls.

Triggered Mode: In the Triggered Mode, a conversion cycle is initiated by the user (or host uP). There are two availabe methods to wake the sensor from sleep mode. The first method (Wake All) is to wake the sensor and perform all three measurement cycles (Z, T and P). This provides completely fresh data from the sensor. The second method (Wake P) is to wake the sensor from sleep and only perform the pressure measurement (P).When using this second method, it is up to the user to interleave Wake All commands at regular intervals to ensure there is sufficiently up to date temperature information. Also, the Wake Pressure method is only available from the I2C interface (not available using a SPI interface).
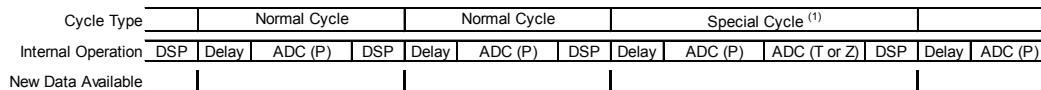
Operation Overview (Cont'd)

Table 1 - DLV Control Logic Detail

Power/ Speed Option	Power/Speed Description	Operating Mode	Over Sample	Delay Between Samples	Normal ADC Cycles	Special ADC Cycles	Special ADC Cycle Interval
		Control Logic					
F	Fast	Free Running	No	No	1 (P)	1 (Z or T)	255
N	Noise Reduced		Yes	No	1 (P)	1 (Z or T)	255
L	Low Power		Yes	Yes	1 (P)	1 (Z or T)	31
S	Sleep[1] (Wake Pressure)	Triggered	No	User Defined	1 (P)	n/a	Never
	Sleep (Wake All)		No	User Defined	1 (P)	2 (Z + T)	Always

Note 1) Wake from sleep with pressure only reading is not available with SPI interface (I2C only).
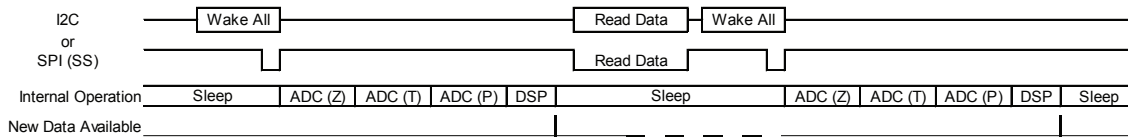
Figure 2 - DLV Communication Model

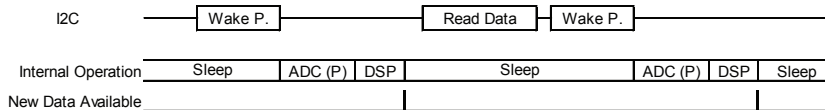**Free Running Mode [(F)ast, (N)oise Reduced and (L)ow Power Option]**

Cycle Type	Normal Cycle	Normal Cycle	Special Cycle [1]	
Internal Operation	DSP | Delay | ADC (P)	DSP | Delay | ADC (P)	DSP | Delay | ADC (P) | ADC (T or Z)	DSP | Delay | ADC (P)
New Data Available				

Note 1: See Table 1 for frequency of Special Cycles

Triggered Mode - Wake All [(S)leep Option]

I2C or SPI (SS)	Wake All	Read Data	Wake All	
Internal Operation	Sleep	ADC (Z) | ADC (T) | ADC (P) | DSP	Sleep	ADC (Z) | ADC (T) | ADC (P) | DSP | Sleep
New Data Available				

Triggered Mode - Wake Pressure [(S)leep Option]

I2C	Wake P.	Read Data	Wake P.	
Internal Operation	Sleep	ADC (P) | DSP	Sleep	ADC (P) | DSP | Sleep
New Data Available				

Digital Interface Data Format

For either type of digital interface, the format of data returned from the sensor is the same. The first 16 bits consist of the 2 Status bits followed by the 14-bit the pressure value. The third byte provides the 8 most significant bits of the measured temperature; the fourth byte provides the 3 least significant bits of temperature, followed by 5 bits of undefined filler data. With either interface, the host may terminate the transfer after receiving the first two bytes of data from the sensor, or following the third byte (if just the most-significant 8 bits of temperature are needed). Refer to Table 2 for the overall data format of the sensor. Table 3 shows the Status Bit definition.

Table 2 - Output Data Format

D[31:30]	D[29:24]	D[23:16]	D[15:8]	D[7:5]	D[4:0]
S[1:0]	P[13:8]	P[7:0]	T[10:3]	T[2:0]	X[4:0]
Status	Pressure MSB	Pressure LSB	Temperature MSB	Temperature LSB	Filler bits (Undefined)

Bit Definitions:
Status (S): Normal/command / busy / diagnostic
Pressure (P): Digital pressure reading
Temperature (T): Compensated temperature reading

Table 3- Status Bit Definitions

[00]	[01]	[10]	[11]
Current Data, no errors.	(Reserved)	Stale Data: Not updated since last read.	Error Condition: electrical fault or configuration invalid.

I2C Interface

I2C Communications Overview

The I2C interface uses a set of signal sequences for communication. The following is a description of the supported sequences and their associated pneumonic. Refer to Figure 3 for the associated usage of the following signal sequences.

Bus not Busy (I): During idle periods both data line (SDA) and clock line (SCL) remain HIGH.

START condition (ST): A HIGH to LOW transition of SDA line while the clock (SCL) is HIGH is interpreted as START condition. START conditions are always set by the master. Each initial request for a pressure value has to begin with a START condition.

Slave address (An): The I²C-bus requires a unique address for each device. The DLV sensor has a preconfigured slave address (0x28). After setting a START condition the master sends the address byte containing the 7 bit sensor address followed by a data direction bit (R/W). A "0" indicates a transmission from master to slave (WRITE), a "1" indicates a datarequest (READ).

Acknowledge (A or N): Data is transferred in units of 8 bits (1 byte) at a time, MSB first. Each data-receiving device, whether master or slave, is required to pull the data line LOW to acknowledge receipt of the data. The Master must generate an extra clock pulse for this purpose. If the receiver does not pull the data line down, a NACK condition exists, and the slave transmitter becomes inactive. The master determines whether to send the last command again or to set the STOP condition, ending the transfer.

DATA valid (Dn): State of data line represents valid data when, after a START condition, data line is stable for duration of HIGH period of clock signal. Data on line must be changed during LOW period of clock signal. There is one clock pulse per data bit.

DATA operation: The sensor starts to send 4 data bytes containing the current pressure and temperature values. The transmission may be halted by the host after any of the bytes by responding with a NACK.

STOP condition (P): LOW to HIGH transition of the SDA line while clock (SCL) is HIGH indicates a STOP condition. STOP conditions are always generated by the master.
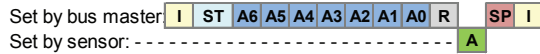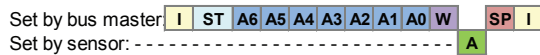
I2C Communications Overview (Cont'd)

Figure 3 - I2C Communication Diagram
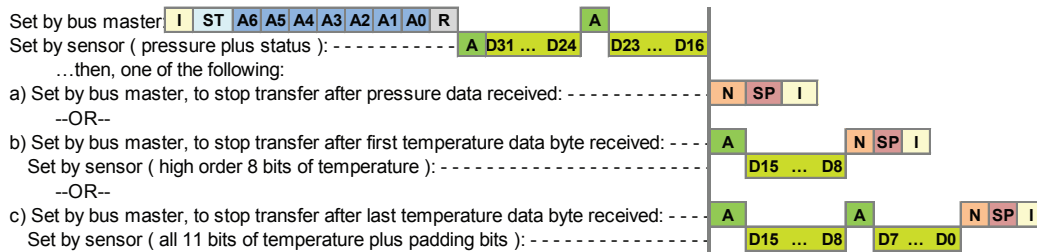
I2C Communications Diagram

1. Start All (to wake sensor from Sleep mode, Zero ADC, read Temperature and read Pressure)

2. Start Pressure (to wake sensor from Sleep mode and read Pressure only)

3. Read Data (with examples of reading pressure, pressure plus 8 bits of temperature and pressure plus 11 bits of temperature)

Figure 3 illustrates the sequence of signals set by both the host and the sensor for each command. Note that for the DataRead command, the host has the option of responding to the second or third bytes of data with a NACK instead of ACK. This terminates the data transmission after the pressure data, or after the pressure data and upper byte of temperature, have been transmitted. See Figure 6 for the I2C timing details.

I2C Command Sequence

Depending on whether the Fast, Noise Reduced, Low-Power, or Sleep options have been selected, the command sequence differs slightly. See Figure 3 for details of the three I2C commands.

Fast, Noise Reduced or Low-power Configuration

The part enters Free Running mode (see table 1) after power-up: it performs an initial complete measurement, writes the calculated data to the output registers, sets the INT pin high, then goes to sleep. After a delay determined by the update rate option, the part will wake up, perform measurements, update the output registers, then go back to sleep. DataRead is the only command recognized in this Free Running Mode. If the INT pin is ignored, the host processor can repeat the DataRead command until the Status bits indicate an updated reading. Note: The INT pin is not available on the SIP version packages (ExBS versions).

Sleep Configuration

The part enters Triggered mode (see table 1) after power-up, and waits for a command from the bus master. If the StartAll command is received, the temperature, ADC zero, and pressure readings are all measured, and correction calculations are performed. When valid data is written to the output registers, the INT pin is set high, and the processing core goes back to sleep. The host processor then sends the DataRead command to shift out the updated values. If the INT pin is not monitored, the host can poll the output registers by repeating the DataRead command until the Status bits indicate that the values have been updated (see Tables 2 and 3). The response time depends on configuration options (refer to Table 1 and Performance Characteristics).

Depending on the application, pressure measurements may be performed by sending the StartPressure command, which only measures the pressure value and uses previously measured temperature data in calculating the compensated output value. This presents the result faster (in about 1/3 the delay time) than the StartAll command. This can be a useful method to synchronize the sensor with the host controller as well as attaining the fastest overall response time without Special cycles occuring at unwanted times. The system designer should determine the interval required for sending StartAll commands, necessary to refresh the temperature and zero point data, in order to maintain accurate output values.

I2C Exceptions

1. Sending a Start condition, then a Stop condition, without any transitions on the CLK line, creates a communication error for the next communication, even if the next start condition is correct and the clock pulse is applied. A second Start condition must be set, which clears the error and allows communication to proceed.

2. The Restart condition—a falling SDA edge during data transmission when the CLK clock line is still high— creates the same stall/deadlock. In the following data request, an additional Start condition must be sent for correct communication.

3. A falling SDA edge is not allowed between the start condition and the first rising SCL edge. If using an I2C address with the first bit 0, SDA must be held low from the start condition through the first bit.

SPI Interface

SPI Command Sequence

DLV sensors using the SPI interface option provide 3 signals for communication: SCLK, SS (Slave Select), and MISO. This read-only signaling uses a hardware protocol to control the sensor, differing slightly with the speed/power option selected as described below:
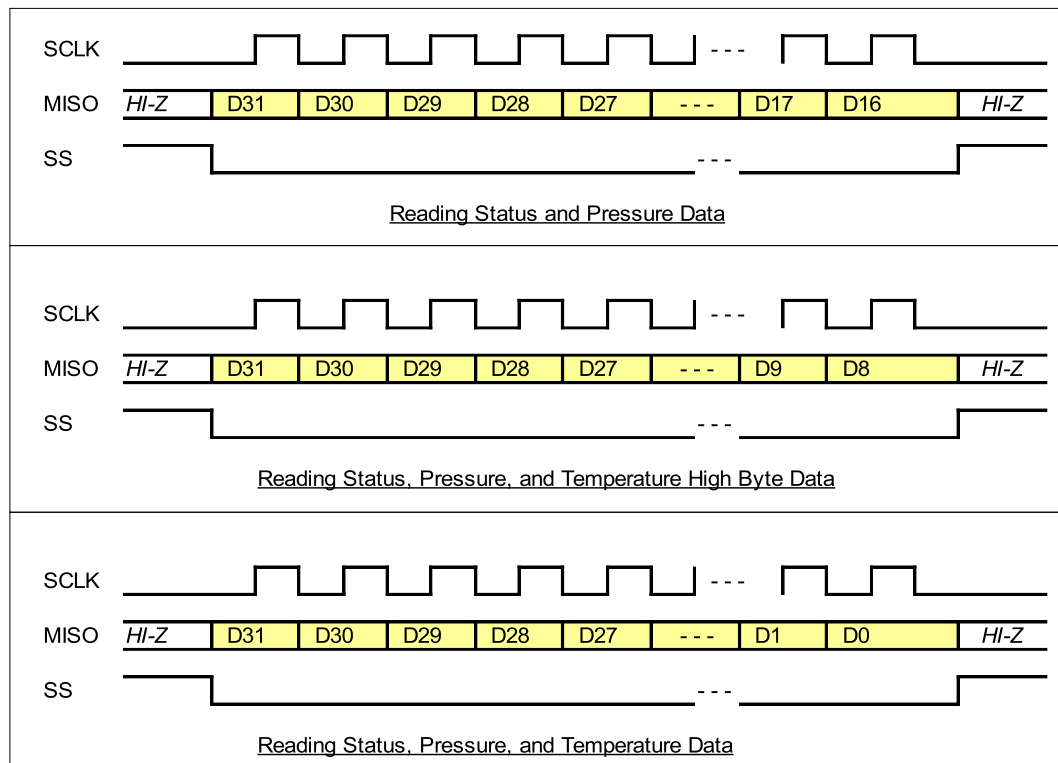
Fast(F), Noise Reduced(N) and Low-Power(L) Configurations: After power-up, the part enters Free Running mode and begins its periodic conversion cycle, at the interval determined by the programmed Power/Speed option. This is the simplest configuration. The only bus interaction with the host is the SPI DataRead operations. Polling the sensor at a rate slower than the internal update rate will minimize bus activity and ensure that new values are presented with each transfer. Note that the Status bits should still be checked to verify updated data and the absence of error conditions.

Sleep(S) Configuration: As with the I2C option, the part enters Triggered mode after power-up, and waits for a command from the bus master. To wake the part and start a measurement cycle, the SS pin must be driven low by the host for at least 8usec, then driven high. This can be done by shifting a dummy byte of 8 bits from the sensor. This bus activity can be considered the SPI StartAll command, where the rising edge of SS is the required input to start conversion. Updated conversion data is written to the output registers after a period dependent on configuration options (see Performance Characteristics). After this update of the registers, the core goes to an inactive (sleep) state. The DataRead command simply consists of shifting out 2, 3, or 4 bytes of data from the sensor. The host can check the Status bits of the output to verify that new data has been provided. The part remains inactive following this read operation, and another StartAll operation is needed to wake the part when the next conversion is to be performed.

SPI Bit Pattern

The sequence of bits and bus signals are shown in the following illustration (Figure 4). Refer to Figure 5 in the Interface Timing Diagram section for detailed timing data. As previously described, the incoming data may be terminated by raising SS after 2, 3, or 4 bytes have been received as illustrated below.

Figure 4 - SPI Bit Pattern

Reading Status and Pressure Data

Reading Status, Pressure, and Temperature High Byte Data

Reading Status, Pressure, and Temperature Data

Figure 5 - SPI Timing Diagram

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS
SCLK clock frequency (4MHz clock)	f_{SCLK}	50		800	kHz
SCLK clock frequency (1MHz clock)	f_{SCLK}	50		200	kHz
SS drop to first clock edge	t_{SSCLK}	2.5			us
Minimum SCLK clock low width	t_{LOW}	0.6			us
Minimum SCLK clock high width	t_{HIGH}	0.6			us
Clock edge to data transition	t_{CLKD}	0		0.1	us
Rise of SS relative to last clock edge	t_{CLKSS}	0.1			us
Bus free time between rise and fall of SS	t_{IDLE}	2			us

Figure 6 - I2C Timing Diagram

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS
SCL clock frequency	f_{SCL}	100		400	kHz
Start condition hold time relative to SCL edge	t_{HSTA}	0.1			us
Minimum SCL clock low width	t_{LOW}	0.6			us
Minimum SCL clock high width	t_{HIGH}	0.6			us
Start condition setup time relative to SCL edge	t_{SUSTA}	0.1			us
Data hold time on SDA relative to SCL edge	t_{HDAT}	0			us
Data setup time on SDA relative to SCL edge	t_{SUDAT}	0.1			us
Stop condition setup time on SCL	t_{SUSTP}	0.1			us
Bus free time between stop condition and start cond.	t_{IDLE}	2			us

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How to Order

Refer to Table 4 for configuring a standard base part number which includes the pressure range, package and temperature range. Table 5 shows the available configuring options. The option identifier is required to complete the device part number. Refer to Table 6 for the available options.

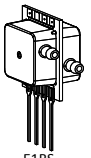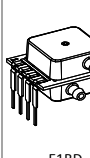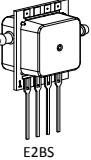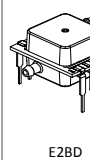Example P/N with options: DLV-005G-E1BD-C-NI3F

Table 4 - How to Configure a Base Part Number

ORDERING INFORMATION	SERIES		PRESSURE RANGE		PACKAGE							TEMPERATURE RANGE	
					Base	Port Orientation		Lid Style		Lead Type			
	ID		ID	Description	ID	ID	Description	ID	Description	ID	Description	ID	Description
	DLV		005G	0 to 5 PSI	E	1	Dual Port Same Side	B	Barbed	S	SIP	C	Commercial
			015G	0 to 15 PSI		2	Dual Port Opposite Side			D	DIP	I	Industrial
			015A	0 to 15 PSIA									
			030G	0 to 30 PSI									
			060G	0 to 60 PSI									
			060A	0 to 60 PSIA									
Example	**DLV**	-	**005G**		**E**	**1**		**B**		**S**		**C**	

Table 5 - How to Configure an Option Identifier

ORDERING INFORMATION	COATING		INTERFACE		SUPPLY VOLTAGE		SPEED/POWER	
	ID	Description	ID	Description	ID	Description	ID	Description
	N	No Coating	I	I2C	3	3.3V	F	Fast
	P	Parylene Coating	S	SPI	5	5.0V	N	Noise reduced
							L	Low Power
							S	Sleep Mode
Example	**N**		**I**		**3**		**F**	

TABLE 6: Available E-Series Package Configurations

Port Orientation	Non-Barbed Lid				Barbed Lid			
	Lead Style				Lead Style			
	SIP	DIP	J Lead SMT	Low Profile DIP	SIP	DIP	J Lead SMT	Low Profile DIP
Dual Port Same Side	N/A	N/A	N/A	N/A	E1BS	E1BD	N/A	N/A
Dual Port Opposite Side	N/A	N/A	N/A	N/A	E2BS	E2BD	N/A	N/A
Single Port (Gage)	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A

Package Drawings

E1BS Package
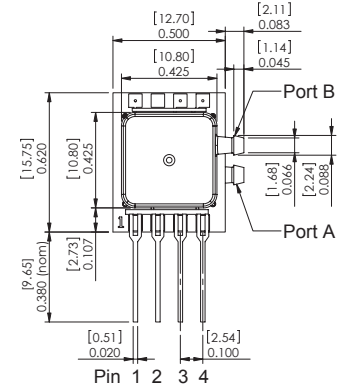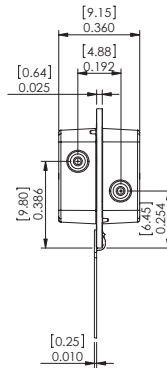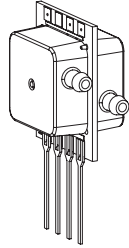
Pinout
1) Gnd
2) Vs
3) SDA
4) SCL

[9.15] 0.360
[4.88] 0.192
[0.64] 0.025
[9.80] 0.386
[6.45] 0.254
[0.25] 0.010

[12.70] 0.500
[10.80] 0.425
[2.11] 0.083
[1.14] 0.045
[15.75] 0.620
[10.80] 0.425
[9.65] 0.380 (nom)
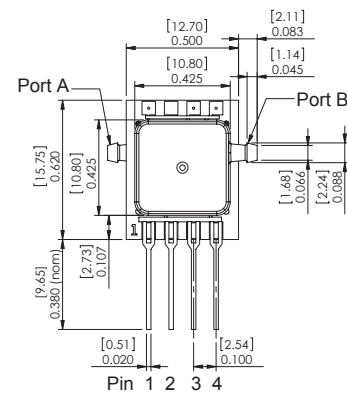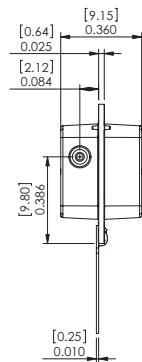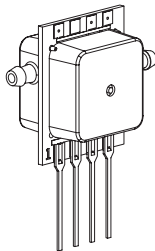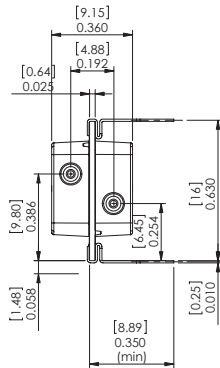[2.73] 0.107
[1.68] 0.066
[2.24] 0.088
[0.51] 0.020
[2.54] 0.100

Port B
Port A

Pin 1 2 3 4

NOTES
1) Dimensions are in inches [mm]
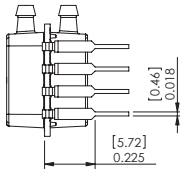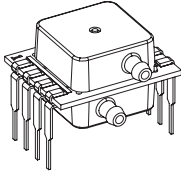2) For suggested pad layout, see drawing: PAD-01

E2BS Package

Pinout
1) Gnd
2) Vs
3) SDA
4) SCL

[9.15] 0.360
[0.64] 0.025
[2.12] 0.084
[9.80] 0.386
[0.25] 0.010

[12.70] 0.500
[10.80] 0.425
[2.11] 0.083
[1.14] 0.045
Port A
Port B
[15.75] 0.620
[10.80] 0.425
[9.65] 0.380 (nom)
[2.73] 0.107
[1.68] 0.066
[2.24] 0.088
[0.51] 0.020
[2.54] 0.100

Pin 1 2 3 4

NOTES
1) Dimensions are in inches [mm]
2) For suggested pad layout, see drawing: PAD-01

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F 408 225 2079
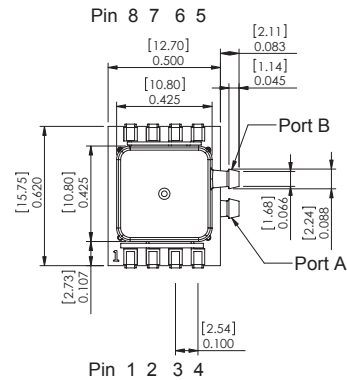
P 408 225 4314

A 16035 Vineyard Blvd. Morgan Hill, CA 95037
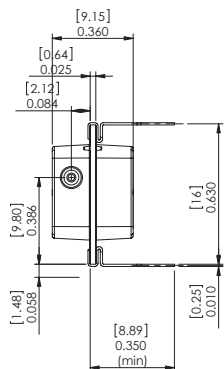
Package Drawings (Cont'd)

E1BD Package

Pinout
1) Gnd
2) Vs
3) SDA/MISO
4) SCL/SCLK
5) INT/SS
6) Do Not Connect
7) Do Not Connect
8) Do Not Connect

[0.46]
0.018

[5.72]
0.225

[9.15]
0.360

[4.88]
0.192

[0.64]
0.025

[1.6]
0.630

[9.80]
0.386

[6.45]
0.254

[1.48]
0.058

[0.25]
0.010

[8.89]
0.350
(min)

Pin 8 7 6 5

[2.11]
0.083

[1.14]
0.045

[12.70]
0.500

[10.80]
0.425

Port B

[15.75]
0.620

[10.80]
0.425

[1.68]
0.066

[2.24]
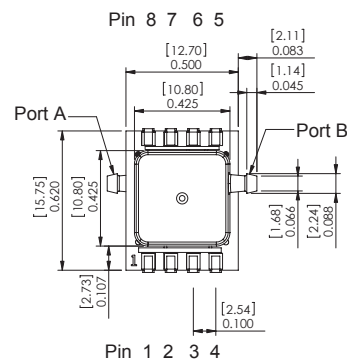0.088

Port A

[2.73]
0.107

[2.54]
0.100

Pin 1 2 3 4

NOTES
1) Dimensions are in inches [mm]
2) For suggested pad layout, see drawing: PAD-03

E2BD Package
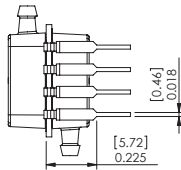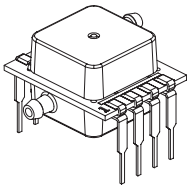
Pinout
1) Gnd
2) Vs
3) SDA/MISO
4) SCL/SCLK
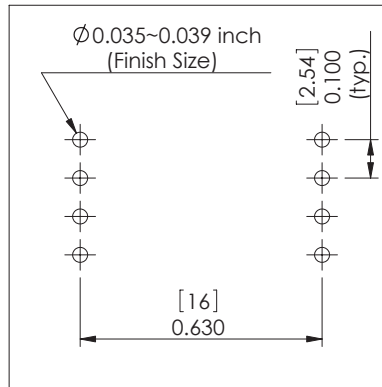5) INT/SS
6) Do Not Connect
7) Do Not Connect
8) Do Not Connect

[0.46]
0.018

[5.72]
0.225

[9.15]
0.360

[0.64]
0.025

[2.12]
0.084

[1.6]
0.630

[9.80]
0.386

[1.48]
0.058

[0.25]
0.010

[8.89]
0.350
(min)

Pin 8 7 6 5

[2.11]
0.083

[1.14]
0.045

[12.70]
0.500

[10.80]
0.425

Port A

Port B

[15.75]
0.620

[10.80]
0.425

[1.68]
0.066

[2.24]
0.088

[2.73]
0.107

[2.54]
0.100

Pin 1 2 3 4

NOTES
1) Dimensions are in inches [mm]
2) For suggested pad layout, see drawing: PAD-03

Suggested Pad Layout

Ø0.035~0.039 inch
(Finished Size)

[2.54]
0.100
(typ.)

PAD-01

Ø0.035~0.039 inch
(Finish Size)

[2.54]
0.100
(typ.)

[16]
0.630

PAD-03

Product Labeling

All Sensors
DLV-005G-
E1BD-C
NI3F
R14N25-01 —— Company

—— Part Number

—— Lot Number
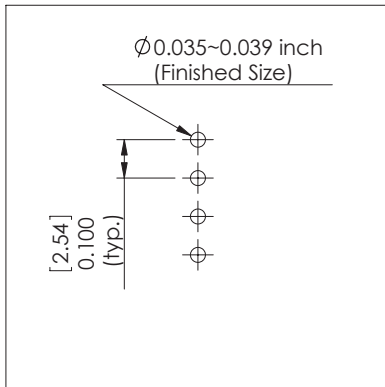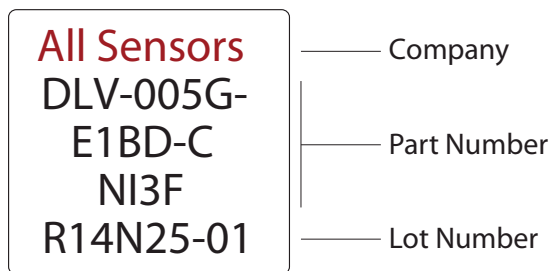
Example Device Label

ALL SENSORS

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